IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chee Kiang Yew, et al.

Art Unit (parent case): 2812

Serial No .:

TBD

Examiner (parent case): Lattin, C.W.

Filed:

Herewith

Docket:

TI-26239.1

For: Direct Attachment of Semiconductor

Chip to Organic Substrate

Conf. No.: TBD

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner For Patents Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Alexandria, VA 22313-1450.

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the listed references are enclosed.

This information disclosure statement is being mailed before receipt of a first office action on the merits in accordance with 37 C.F.R. 1.56, 1.97, and 1.98.

Respectfully submitted.

Michael K. Skrehot

Attorney for Applicants

Reg. No. 36,682

Texas Instruments Incorporated P. O. Box 655474, MS 3999 Dallas, Texas 75265 (972) 917-5653

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PTO/SB/08A (08-00)
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Substitute for Form 1449A/PTO				Application Number	TBD	
	INFORMATI	ON DISCLOSURE		Filing Date	Herewith	
		T BY APPLICANT		First Named Inventor	Chee Kiang Yew, et al.	
	OTATEMEN	II DI AII LIVANI		Group Art Unit	TBD	
	(use as many	y sheets as necessary)		Examiner Name	TBD	
Sheet	1	of	2	Attorney Docket No.	TI-26239.1	

				U.S. PATENT DOCUM	IENTS	
		U.S. Patent D	ocument	Name of Patentee or Applicant of	Date of Pub. of Cited Doc. (mm-dd-yyyy)	
Exam. Initials	Cite No.1	Number	Kind Code ² (if known)	Cited Doc.		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	5,061,509		Naito, et al.	10/29/1991	
	AB	5,353,498		Fillion, et al.	10/11/1994	
	AC	5,739,585		Akram, et al.	04/14/1998	
	AD	5,776,796		Distefano, et al.	07/07/1998	
	ΑĖ	5,981,314		Glenn, et al.	11/09/1999	
	AF	6,218,202 B1		Yew, et al.	04/17/2001	
	AG	6,228,676 B1		Glenn, et al.	05/08/2001	
	AH	6,242,283 B1		Lo, et al.	06/05/2001	
	AI					
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					FOREIGN PATE	NT DOCUMENTS		
		Foreign Patent Document			Name of Patentee	Date of Pub.		
Exam. Initials*	Cite No.1	Office ³	Number⁴	Kind Code ² (if known)	or Applicant of Cited Doc.	of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T [®]
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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					Complete If Known		
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	INFORMATION	ON DISCLOSURE		Filing Date	Herewith		
		T BY APPLICANT		First Named Inventor	Chee Kiang Yew, et al.		
STATEMENT BY AFFLICANT				Group Art Unit	TBD		
	(use as many	sheets as necessary)		Examiner Name	TBD		
Sheet	2	Of	2	Attorney Docket No.	TI-26239.1		

		OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS	
Exam. Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
	CA	"Silicon Processing for the VLSI Era", Stanley Wolf, Ph.D., et al., Volume 1: Process Technology, Lattice Press, Sunset Beach, CA, 1986, pp. 547-551	
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